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FWC (GAU-2741/4)

JC662 U.S. PTO
10/16/99

JC511 U.S. PTO
09/419752
10/16/99

In the United States Patent and Trademark Office

Serial Number: 09/169,462
Appn. Filed: October 09, 1998.
Applicant: Peter Ar-Fu Lam

Appn. Title: Table Format Programming
Examiner/GAU: Dorvil R., 2741

Request for File-Wrapper-Continuing Application

Box FWC
Assistant Commissioner for Patents
Washington, District of Columbia 20231

DOCKET: ESY2A

Sir:
Pursuant to Rule 62, please file a file-wrapper-continuing application of the above pending complete application. It should be a **CONTINUATION-IN-PART** of the above application. This Rule 62 continuing-in-Part application is being actually filed in the PTO or express mailed during the pendency of the above application. Enclosed please find the following:

1. Please use the substituted specification enclosed (including the claims and abstract). 40 Pages and Remark 8 pages.
2. Please use the drawings of the above specification.
2. Enclosed is a new Declaration for the CIP application.
3. A small entity declaration was enclosed for the above application..
4. The filing fee has been calculated as shown below:

CLAIMS				FEES	
Type	Number Filed	Number Extra		Rate	Fee
1. Basic Fee (37 CFR 1.16(a))				$760	$760
2. Total Claims (37 CFR 1.16(c))	91 - 20	=	71 x	$18	$1278
3. Independent Claims (37 CFR 1.16(b)): ~~12~~ - 3		=	9 x	$78	$702
4. Multiple Dependent Claims:				$0	$0.
				TOTAL:	$2740
				SMALL ENTITY TOTAL:	$1370

5. Our check no. 1653 to cover the above items is enclosed.
6. A duplication of this copy.

Very respectfully,

Applicant: _____
20104 Wayne Ave.,
Torrance, CA 90503
(310) 320-9811

Express Mail Label # EH474629426 US **; Date of Deposit: October 16, 1999.**

I hereby certify that this paper or fee is being deposited with the United State, Postal Service using "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to "Box FWC, Assistant Commissioner for Patents, Washington, DC 20231,"

Peter Ar-Fu Lam



In the United States Patent and Trademark Office

Sole Applicant: Peter Ar-Fu Lam
Title: Table Format Programming
Filed: October 16, 1999

Small Entity Declaration - Independent Inventor

As a below named inventor, I hereby declare that I qualify as an independent inventor as defined in 37 CFR 1.9(c) for purposes of paying reduced fees under Section 41(a) and (b) of Title 35 United States Code, to the Patent and Trademark Office with regard to my above-identified invention described in the specification filed herewith. I have not assigned, granted, conveyed or licensed and am under no obligation under contract or law to assign, grant, convey or license, any rights in the invention to any person who could not be classified as an independent inventor under 37 CFR 1.9(c) if that person had made the invention, or to any concern which would not qualify as a small business concern under 37 CFR 1.9(d) or a nonprofit organization under 37 CFR 1.9(e).

Each person, concern, or organization to which I have assigned, granted, conveyed, or licensed - or am under an obligation under contract or law to assign, grant, convey, or license - any right in the invention is listed below:

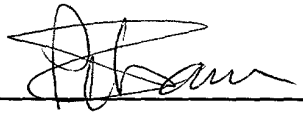
(X) There is no such person, concern, or organization.

() Any applicable person, concern, or organization is listed below:

I acknowledge a duty to file, in the above application for patent, notification of any change in status resulting in loss of entitlement to small entity status prior to paying, or at the time of paying, the earliest of the issue fee or any maintenance fee due after the date on which status as a small entity is no longer appropriate (37 CFR 1.28(b)).

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under section 1001 of Title 18 of the United State Code, and that such willful false statements may jeopardize the validity of the application, any patent issuing thereon, or any patent to which this verified statement is directed.

Name of inventor: Peter Ar-Fu Lam

Signature:  Date: October 16, 1999.

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OCT 16 1999
PATENT & TRADEMARK OFFICE
JC76

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:	:	
PETER AR-FU LAM	:	Examiner: Dorvil R.
Serial No. TBD	:	Group: 2741
Filed: 10/16/99	:	
For: TABLE FORMAT PROGRAMMING	:	Docket: ESY2A

Hon. Commissioner of Patents
and Trademarks

October 16, 1999

Washington, D.C. 20231

Sir,

In response to the final office action of application 09/169,462 dated 06/30/99 and the subsequent telephone interview dated 08/03/99, applicant respectfully submitted a continuation-in-part FWC application, please reconsider the allowance of the application in accordance to the remark and supplemental information submitted as follows:

REMARK:

Claims 1-88 of the parent application 09/169,462 were rejected under 35 U.S.C. §101 as non-statutory subject matter. As indicated in the final office action dated 06/30/99, the examiner agrees the subject application is a method of programming a computing device, that is a practical application within the technological arts. However, it does not disclose specific hardware, specific software, or a combination therefore for performing the claimed function; and the claims, each as a whole, are directed to a abstract idea that is not applied to or limited by other physical elements or process steps.

During the telephone interview dated August 3, 1999, the applicant and examiner reaches an agreement that if a physical transformation, or the pre-computer and post-

computer activities are recited into the independent claims, the rejection under 35 U.S.C. §101 is overcome. Accordingly, all independent claims of the parent applications are amended with this direction and re-submitted in this FWC application. The specification is respectively amended to add more detail in the description of the pre-computer and post-computer activities to supported the amended set of claims. Various typo of the specification are also corrected to provide a better-presented patent when it is to be allowed.

In addition to the above mentioned direction, after discussing with the attorney representing the applicant to file the subject application in EPO, three additional claims are added as new claims 1 to 3. These new claims represents different approach to claim the improvement of the subject application based on the widely used fundamental table format technology disclosed in applicant's issued US Patent 5,867,818 and the corresponding UK Patent GB2306024 issued on August 24, 1999.

For the convenience of the examiner, the applicant respectfully submits that there is basically no new change in the fundamental matters in related to the parent application 09/169,462. Since the specification is enriched with the description of pre-computer and post-computer activities to support the agreed direction of claims; for the avoidance of doubt, the present application is submitted as a CIP application.

Discussion of **EACH** independent claim and how it is improved in accordance to the agreed direction of the telephone interview dated August 3, 1999 is respectfully presented as follow:

Claim 1:

Claims 1 is a newly added claim presented in the European rule of "Prior art plus characterized by improvement" format. The first six lines of the claim recites the configuration state table and the path table presented in the prior art parent application US Patent 5,867,818 and UK Patent GB2306024. The recited inter-related configuration table and path table constituting the Table Format programming method is now a well proven patentable subject matters widely used by many programming engineers worldwide. Evidence of application and publicity such as list of licensees and reference web sites (easyformat.com) had been previously submitted for the reference of the

examiner. The subsequent recitation of claim 1 starting from line 10 of page 25 represents the subsequent improvement of this well established, statutory subject matter. These improvements are the result of many years of further research after the Table Format programming technology was first introduced by the applicant in the US Patent 5,867,818. Accordingly, claim 1 recites the fundamental characteristics of an established, applicable and statutory technology plus the additional improvements. Therefore it should not be viewed as an abstract idea to be rejected under 35 U.S.C. §101. If this logical approach is not to be accepted, the examiner is respectfully requested to rebut the world recognized, logical European patent practice presented in claims 1 to 3 with evidence and supporting reasons.

Claims 4, 7, 29, 35, 63 and 90:

Each of these independent claims is enriched to recite the formation of a programming objective as a pre-computer activity, various table format programming activities are then interactively performed by the programmer during the programming process. As a result the computer delivers the programming objective as the post-computer activity. These claims are amended in accordance of the telephone interview with the examiner on August 3, 1999. The specification has been properly amended to support the inclusion of the pre-computer and post-computer activities into the subject claims.

Claim 44:

Claim 44 is a hardware claim which is quite simple in structure. This claim recites a combined dual processors (or more) apparatus characterized in that the first processor is configured for executing table format program which is defined in the specification. The second processor is just a regular processor configured to execute regular programs. The novelty of this claim is that the first processor is represented by the Table Format processor, such as the one disclosed in applicant's issued parent patent 5,867,818, and for it to be combined with another regular processor to perform other regular jobs. Since this is an apparatus claim, it should not be rejected by 35 U.S.C. §101. Applicant respectfully requests for evidence of prior art reference if this claim is to be rejected.

Claim 50:

For the amended claim 50, the pre-computer and post-computer activities are all well addressed not only in the specification, but also in the claim per the telephone interview with the examiner dated August 3, 1999. There are numerous pre-computer activities. The typical one is to define the programming specification before the programming procedure. The result is generating and converting codes suitable to be executed by an external second computing device.

Claim 69:

Step (1) of claim 69 requires the user to define the virtual qualifier, that is a first pre-computer process before any programming job can be started. Step (7) of claim 69 delivers a coded file executable by said computing apparatus. This code file is an output of the programming process that defines the post-computer result obtained.

Claim 85:

The pre-computer activity is defined by the step to create and define the custom expression. The pre-computer activity is to equate said custom expressions with an official reference expression set interpretable by the computer. The post computer activity is to list the program listing with the selected expression set. The pre-computer and post-computer activities are thus recited in the claim as directed by the telephone interview with the examiner on August 3, 1999.

Claim 91:

This claim corresponds to claim 88 of the parent application. The "means to access a communication link" recited in the amended claim 88 of the parent application is now amended to be a first computing apparatus connected with a second remote computing apparatus which are well described in the specification. This claim is an apparatus claim which should not be identified as an abstract concept. The examiner is respectfully to examine this claim based on the characteristics of the two computing apparatus as directed by the claim wording and the specification which clearly defines the table format programming method. It is submitted that the fundamental version of table format

programming method making use of the technology are disclosed in applicant's issued patents US Patent 5,867,818, UK Patent GB2306024 and the reference documentation of the licensee of Easy Format. Claim 91 adequately recites the characteristics of the fundamental table format programming technology and the improvement claimed.

During the telephone interview on August 3, 1999, the examiner indicates that the issued parent application is an independent patent application and has nothing to do the subject application. The examiner also query that how two or more tables of data can constitute a programming method and thus it is an abstract idea. The applicant wishes to take this opportunity to discuss this position. Firstly, the subject application was submitted right at the beginning that it is a Continuation In Part application of the now issued US Patent 5,867,818. According to MPEP, the parent application should be taken into reference in the examination process of this CIP application. The examiner has the responsibility to make sure that the claims submitted in the subject application does not constitute double patenting. Not only the parent application is to be taken into reference, more important, the technology disclosed in the parent US Patent 5,867,818 is now matured into a successful product which is licensed by six reputable micro-controller manufacturers and being considered by many other manufacturers. It is being used by thousands of programming engineers world wide. Evidence of this commercial success had been previously submitted for the reference of the examiner. Accordingly it is submitted that "in fact" table format programming is now a technology well know in the art, an established programming technology that is statutory.

Concerning the argument if two or more tables can constitute a statutory programming method; first of all, the applicant does not object that simply two or more tables of data does not constitute a programming method. Previously it was true that tables filled with data did not constitute a programming method, until the table programming method was first introduced in the issued US Patent 5,867,818. Close examination of the specification and the claims indicates that the tables of Table Format programming method are not just regular array of data. These tables are governed by some rules and instructions that the elements inside the tables are to be carefully filled in

accordance to the programming objective. The position of the elements inside the table tells the interaction between the configuration table and the path table. These interactions constitute the programmability, which, with a suitable compiler, is understandable by a computing apparatus. Therefore it can be executed by the computing apparatus according to the programming objective of the programmer. Accordingly the table format programming method bridges between the human being and the computing apparatus. It is a method, and also a "tool" for a programmer to program a computing apparatus.

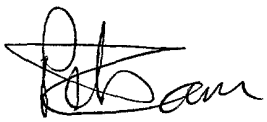
The attention of the examiner is now brought to a programming configuration which is now commonly used in the trade to program Easy Format supported micro-controllers (please refer to the web site easyformat.com previously submitted to the examiner). A computer set up with an Easy Format compiler is input with a table format program targeted for a particular model of micro-controller. The function of the computer is to compile, or translate the table format program into machine codes executable by the target micro-controller. These machine codes are then embedded into the generic micro-controller by mask ROM, EPROM or flash memory. As a result, the micro-controller becomes a "programmed" controller to perform the desirable programming objective. This activity has been clearly disclosed in the specification. When we look into the computer compiling the table format program, the pre-computer activity is for the programmer to define the programming objective, and then to fill up the tables according to the programming rule which had been clearly defined in the specification. The post-computer result is a set of codes executable by the micro-controller external to the compiler computer. Thus a physical transformation is identified. When we looked into the action of the micro-controller, a generic micro-controller. Before programming it is only a piece of dump silicon. The table format compiled codes brings in executable instructions that directs the original passive micro-controller to perform according to the programming objectives of the programmer. Thus physical transformation is observed in the aspect of the micro-controller. Accordingly the programmability characteristics of table format programmed tables, which are very different when compared with the regular data tables, are identified. Therefore the well-characterized tables of table format programming should not be classified as abstract ideas, in view of the abundant supporting evidence submitted and the great success it has in the field.

It is important to note that the table format programming technology successfully changed the tradition view that tables are only for data, that is non-statutory. It really depends on how the table is originated and formulated. This is also the beautiful part of the invention process. As it is named, creative invention changes the tradition looking of matters. As the patent system is designed to encourage invention, we should look carefully into the fundamental elements how the matters is changed by the invention, rather than drawing a conclusion according to tradition wisdom that tables are all data and therefore are abstract ideas. If tables are carefully programmed by programmers in accordance to the rules and characteristics of the table format programming method disclosed in the specification and characterized in the claims, they are absolutely real working programming method and not abstract ideas. This is proven by the great success of Easy Format programming in the field. In fact, considerable tax revenues submitted to the U.S. government are being generated from the royalty received by licensing the invented table format or Easy Format technology.

The subject application summarized the subsequent improvements of the fundamental table format technology invented four years ago. It came from the result of three years of intense research targeted to extend the servicing scope of the fundamental technology. The examiner is respectfully solicited to consider allowance of the application in accordance to all the evidence and remarks submitted. If rejection is to be withheld, the examiner is respectfully requested to provide evidence why the widely successful fundamental table format programming method is non-statutory, and why the improvements of a widely used fundamental table programming method is not statutory.

Since patentable and practical applications in the technology art are identified and agreed, the modification of the claims to overcome the 35 USC §101 rejection is a matter of formality. The Applicant hereby respectfully requests the Examiner to propose broadest allowable claim or specification amendment to the application pursuant to MPEP 707.07(j) if further amendment to any claim is required for the application to be allowed.

Respectfully submitted by,



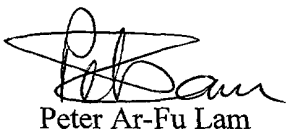
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Peter Ar-Fu Lam

DEPOSIT ACCOUNT

OIPE JC78 OCT 16 1999 PATENT & TRADEMARK OFFICE

TITLE

Table Format Programming

INVENTOR:

Peter Ar-Fu Lam

RELATED APPLICATIONS

This is a continuation in part of U.S. Application No. 08/538,426 filed October 02, 1995 now issued as US Pat 5,867,818 and FWC of U.S. Application No. 09/169,462 now abandoned. Reference is also directed to applicant's UK Patent GB2306024 issued on August 24, 1999. These applications are incorporated herein by reference.

FIELD OF THE INVENTION

The present invention relates to a method to program a computing device by filling up different kinds of tables to achieve the programming function and facilitate the control of program flow.

BACKGROUND OF THE INVENTION

The present invention relates generally to a programming tool that is designed to interface between programmers and computers. This programming tool is also referred as a programming method that makes extensive use of tables to represent the logical thinking of a programmer, and enabling the programming process easily to be understood by third parties. Thus, these improvements enhance the efficiency of programming, reduce the likelihood of the presence of program bugs or structural errors. In addition, the training cost required for a programmer to learn the programming method is minimal. The resulting programs composed with the invented method also will be easy to be read and to be maintained by any programmer.

Traditional programming languages define a set of programming instructions and programming rules. Most commonly used programming languages such as BASIC, C and JAVA are written in the form of a top down line by line listing in sequence. In many

applications, hundreds of pages of codes are written to describe the job function. It is extremely difficult for a programmer to follow the logical flow of a program from a long listing. The size of programs therefore causes difficulties in subsequent maintenance work. Besides, multiple pages of programs filled with sequential lines of codes are very difficult to be understood by another programmer who has not previously involved in the job. Although many compact instructions or programming symbols had been invented, the size of many programs written in many different programming languages are still too big to be easily interpreted by a professional programmer. In many situations, the author of a program also finds difficulty to understand his or her own program after an extended period of time. The requirement of compact size in instruction set and readability contradicts to each other. It is highly desirable to have a programming method, which provides compact program size and is also simple to be understood by other programmers, preferably for the inexperienced people as well.

Every time when a new programming language is invented, coding symbols and instruction set are designed to describe a programming function. Rules are also designed to restrict how the programming instructions are to be used in order to make a program written with the language interpretable and executable by a computing device. In many situations, the instruction manual describing a programming language is over two inches thick in order to document all these instruction set and describe the programming rules. It is not unusual to require months of training effort to be required in order for a programmer to learn the whole instruction set and understand all the programming rules of a new programming language. More programming tricks are usually learned through subsequent years of programming experience.

Although many programming languages allow a program to include conditionally jump, call functions or branches to other segments of the program according to the structural requirements of the job, the logic flow of these interactive branching activities inside the program structure is extremely difficult to be interpreted by a professional programmer even though a great amount of time may have been spent in reading the program.

There are various kinds of programming languages available in the market, each dedicated to a certain kind of application, or designed according to a particular

programming environment. Many other programming languages are designed to program specific hardware configurations or system. Often, different programming languages make use of different symbols or even expression strings to represent an instruction describing a similar function. A programmer may often be confused which symbols or expressions to be used, after learning many different languages, each with different format and instruction expressions. It is highly desirable to have a new programming tool enabling a programmer to overcome this difficulty.

Because most programming languages are designed optimally to interpret the programming function specific to their application environment, many programming jobs which includes a large variety of functions are best to be dissected into modules, with different modules written in different languages, according to their specific requirements. It is therefore highly desirable to have a special programming method suitable for managing the integration of program segments written in different languages. With the popularity of internet, it is also highly desirable to have a generic programming method suitable for coordinating programming modules of different format to form a combined program, which is suitable for downloading the program to individual local computers remote from the host computer. For this type of application, the program must be of compact nature and be able to self-reconfigure according to the nature of the various local computers.

Applicants issued US Patent 5,867,818 and UK Patent GB2306024 first introduced a primitive table format method for programming a hardware controller chip having multiple input and output terminals. The present application is directed to the various improvements achieved from the further intense research since this primitive structure was invented.

SUMMARY OF THE INVENTION

The present invention is directed to the inventive steps of providing the architecture of a generic model to program a computing device making use of the concept of table format programming. One objective of the invention is to establish a generic model of programming method to direct the operation of a computing device and structure this model such that it is simple enough to be applied by ordinary people without receiving an intensive professional training. A further objective is establish a programming model such that any program written with this format can be easily understood by another person without having extensive experience in the language or close involvement with the writing of the program. Another objective of the invention is to establish a programming model that clearly identifies the structure of a programming job through the coding process, therefore enabling the program to be maintained with minimal effort. As a result, considerable training cost to learn a programming language, time cost to write a program, the time cost to debug a program and future maintenance cost to modify the program can be reduced. With such a user-friendly programming structure, it is expected that fewer programming bugs will be included during the programming process and therefore the time cost required for debugging can also be reduced.

Another further objective of the invention is to establish a structural programming format which is able to express a clear skeletal structure of the job while enabling the individual specific functional program modules to be selectably programmed with any language most suitable for that function. This approach further enhances the efficiency of programming and reduces the amount of future maintenance work required.

The programming method of the present invention comprises the steps of filling up two or more tables with program data. Various types of tables are designed to perform different supporting functions. A table is defined as a matrix of data. A table may have one or more dimensions. A table matrix is typically represented by m row and n columns where m and n are integers equal or greater than one. Each element of the table may be represented by a label, a single expression or a sequence of expressions.

In a first embodiment of the invention, the programming method introduces the concept of a task table which comprises a table of data formed to control the activity of multiple tasks. More than one task table may be present in a program, each table comprises of one or more task states. Whenever a task state is specified in the programming process, the situation and/or activities of the tasks specified are defined. Further task priority information can be included in the activity task table or provided in another separate table for the computing device to assign handling priority while running multiple tasks with the shared resources of the computing device.

In the fundamental aspect of the invention, a table is formed to define one or more configuration states which may include input and/or output information. When an input qualifying condition of a configuration state is satisfied, it will be cause a response of a specific sequence of action represented in another table called an event table or path table. At least one configuration state is to be specified as the active state. This early version of Table Format programming method designed for micro-controllers was described in applicant's issued US Patent 5,867,818 and UK Patent GB2306024. The present application is directed to various enhancement of the fundamental concept resulted from years of intense research to improve the servicing scope of the technology.

In another extended embodiment of the invention, a further table is formed to define the qualifying conditions required to trigger a qualifier configuration state. In this embodiment, the concept of virtual qualifier is first introduced. A virtual qualifier is defined as any qualifying condition not coming from a physical hardware terminal. Virtual qualifier is important as it includes the consideration of a result which comes from a single software instruction or from a whole software program. Virtual qualifier may also include hardware internal to a computing device such as overflow of an internal register. It may be triggered by the result of an internal software or hardware interrupt, generation of a sign, polarity signal, signal indicated the presence of data from another system or the presence of a flag. Other commonly used example of virtual qualifier is the output of a mouse driver program which specifies the direction of motion of a pointer according to the movement of a mouse.

In another embodiment of the invention, a table is formed to define the output configuration of an output state. The output state may be a signal to be sent out through a

physical hardware terminal or the configuration of a virtual computing output states. A virtual computing output state is defined to be any output state which does not describe the activity of a hardware terminal. An example of a virtual computing output state is the triggering of a software program to start running, or to set particular conditional parameter to control a software, or for the software to perform some specific function.

When a qualified trigger is obtained, a responsive action is executed. A path table or event table is a table grouping one or more responsive actions. A path may represent an action or a sequence of actions to be performed when a qualified condition is received. A path may be initiated by another path. This responsive action or sequence of action is named as a path equation. The interactive composition of state tables and path table is analogous to event structure illustrated with state diagrams. Besides, table format programming offers a program presentation which better follow the thinking of human mind and therefore makes it more user friendly. In order to better present the descriptive presentation of the program flow, each path in the path table can be assigned with a meaningful label according to the wish of the programmer. A meaningfully named label can be assigned to a configuration state or a path. All these labels helps the programmer or other people to understand the action and logic flow of the program written. This is an important contribution of the present invention to help interpret a composed program and reduce future maintenance costs.

Another table which can be included in the invented programming process directs an output condition in an output state to a specific sequence of operation. This sequence can be represented by another table to simplify the programming process.

A further type of table introduced in the present invention enables the programmer to change the expression and/or syntax of the table format programming language into any other expressions or symbols desired. Further tables which may be included define one or more sets of library or external programs required to support the programming job.

With the support of the table format programming tool, the traditional practice of programming is significantly changed. When the structural programming job is started, the programmer interactively describe the skeletal structure of the program flow with tables of states and paths. The programmer is free to assign meaningful labels or names to

represent each configuration state and path. The meaningful names of each state configuration and path equation helps to describe the flow of the program. When an action to be included in a path is too complicated to be represented by the available instruction set, or the action is better to be described with a program module of another language, the programmer is free to assign a meaningful label to represent this desired action. At the end of the programming process, the programmer should furnish executable program modules or expressions for each of the undefined labels assigned. In this way program modules are formed by interactively organizing multiple groups of tables. A program module having at least a state table and a path table is named as a program group or table group. Each program group can be assigned another meaningful label to describe the function of the program module. This label can then be utilized in another program module or group for pointing to this program group when a jump or function call is required. The very first program group to be executed when the program is started by default is named as the "main" group for convenience. Inside each group, there should be a "start" up path to serve as the starting path when the program is initialized or starts from default. In order to improve programming efficiency, supporting program modules may be composed with a desirable language different from the table format programming method used in the main group. Many different languages may be utilized in the same program. It is highly desirable to design in features in the table format programming method to pass parameters and variables in between program modules of different languages. In a preferred embodiment, a table format program is designed to interface with different programming languages and serves as a bridge to communicate in between the program modules written in different languages. The program eventually composed can then be compiled or translated into a specific desired programming language, including Assembly Language or even machine code digital data. These codes can be executed by the compiling computer or to be transplanted to run in another computing device. In the later case, the compiling computer serves as a tool, or work station to compile the table format oriented program intended to run in another computing devices, such as embedded controllers, or third party computers. It can be observed that the table format programming language significantly changes the concept and practice of writing a program. A program is written under a logical and well-

structured manner. By making use of the freedom to assign meaningful labels, the program flow is as smooth as composing a composition to describe the programming job. Each label is then linked with an external program module which can be written in any languages desired. From time to time, multiple levels of program groups or modules can be arranged. Because the program size of each table format group is usually quite small and the program is well structured, the aforementioned advantages of table format programming can be easily achieved.

Freedom to assign or equate labels and instruction keywords is very important for Table Format programming to support multiple languages programming platform. This is because many languages may make use of different syntax for the same kind of application. The freedom of labeling and equating feature enables the user to unify the labels and syntax according to his/her desire.

The nature of table format programming makes it a perfect supporting tool to enhance the programming structure of another language. For example, the compiler of a high level language can be modified to include just sufficient the table format programming function to provide the skeletal structural of a program written in that language.

Since the method of table format programming is unique and independent to the third language programs it coordinates, the program management job can be simplified by providing a simple table format coprocessor which handles almost all the table format programming tasks. This structure of multiple processors will relieve the workload of the main processor and allow it to focus on handling the regular jobs and improve the overall system performance, particularly in multitasking environment. The table format coprocessor can be a processor located outside the main processor or may coexist with the main processor within the same integrated circuit chip. Applicant's US Patent 5,867,818 and UK Patent GB2306024 substantially disclosed a table format processor specially designed to serve this application. Other microprocessors such as those advertised to be supported by Easy Format in the market can be easily converted into a table format processor by supporting with a suitable table format compiler during programming. When table format programming is applied to microprocessors, micro controllers or any other type of embedded controllers, the compiled or encoded table

format program becomes digital data which is stored in memory such as ROM, EPROM or flash memory for the execution of the microprocessor or micro controller. A printed circuit assembly comprising the processor and memory storing the compiled representation of the table format program is then used to build up a finished commercial product for point of sale distribution.

In accordance with the structural organization of the table format programming, it is particularly suitable for event driven applications such as programming under a windows environment, web site, interactive game or control programming. An important characteristic of table format utilizing in event driven programming is that it is a highly compressed format to represent a program. Most table format programs for small jobs are less than one page long. Experimental results demonstrated that one page in table format programming may represent eight pages of assembly language program depends on the complexity of the available instruction set. Considerable code size saving can also be achieved when compared with many high level languages. This nature makes table format programming economical to serve as a media to communicate through a limited bandwidth communication channel; such as in between a host system to communicate with numerous local computers in the network or internet applications. It should be noted that a network, communication link or communication channel refer to any means connecting two computing devices together, including serial port, parallel port, USB port, internet, intranet, extranet, LAN and any communication means interfacing two computing devices. The devices at the two ends can be connected by wired, wireless or mixed mode connection. In internet programming, a further step is required to evaluate the system configuration of an user computer and then adjust the program settings before a down load program can be executed by the local computer. The configuration of many computer to human interface devices such as monitor, graphic card, sound generation device, pointer control, game controller control are among the configuration settings to be defined. Because the internet communication line is usually the bottle neck of a system, it is recommended that the compiling job for table format programming is performed at the local computer level in order to make use of the advantages of the compressed codes of table format programming.

Figure 11 is a block diagram showing how a local computer 801 is connected with a remote computer 803. In many applications, a table format program is stored in the local computer. It is downloaded to a remote computer through a communication link 802 upon request. The table format program can be stored in the memory means 804 of the remote computer or immediately compiled by the remote computer. The compiled file is an executable file to perform some predefined work at the remote computer. This executable file can also be stored in the memory means 804 of the remote computer.

When a compiler is designed to handle table format programming, it is highly recommended to make use of key words to identify the nature and location of each table, and the corresponding group accordingly. Task, group, qualifier, state, path and library are exemplary keywords used to identify the functional tables in the illustrated embodiments. It should be noted that the compiling, translating, interpreting or transforming of the table format programming method includes the process of converting the table format program into any other program format such as machine language or any higher level language. The conversion process can be performed by a compiler program written in another programming language or supported by an executable table format program as well. The compiling process can be conducted at the computer running the task or at a separated computer set up for the programming development or compiling process. For the convenience of debugging, a debug tool may be configured to reference a particular location of the table format program. The corresponding location of the program is then translated into another programming language. For example, a user can simply request the transmission of a data chain in a table format program. After the table format program is translated into Assembly Language, the user is able to locate the transmission process in the Assembly code listing and then modify the transmission characteristics of the UART as desired. This debug tool is very helpful when it is preferred to fine tune a particular process in the environment of another programming language.

The concept of task table effectively extends the convenience of table format programming to multitask environment as well.

The novel features of the invention are set forth with particularly in the appended claims.

The definition of computing device refers to any device having computing capabilities, including computers, micro controllers and embedded controllers, microprocessors, printed circuit assembly comprises of micro controllers or microprocessors. In addition to computers, other supporting hardware required to support the invented technology are the debugging hardware, communication links such as cables, communications ports, hubs and networks as defined in the specification. The table format program may be displayed on display terminals, printed on printers, and encoded as digital data. The compiled or encoded digital data representing the table format program may be stored in any memory device such as RAM, ROM, disk drive, and CD ROM. The technical terms, keywords and labels used in the embodiments are exemplary and numerous modifications, specification variations and table format rearrangements can be readily envisioned to achieve an equivalent result, all of which are intended to be embraced within the scope of the appended claims. The invention will be best understood from the following description when read in conjunction with the accompanying drawings.

In order to fully utilize the advantages of the invented technology, users are required to carefully prepare for the pre-computer or pre-programming activities. Typical pre-computer activities involves defining the programming objective, analyze the program job specification. Programming objective comprises of any kinds of acts the programmer wishes the computer to perform, such as performing a task, providing some kinds of output, through a screen, a printer or a speaker in accordance to input received, or the result of the task. Users may choose to reduce the project into Table Format coherent state diagram. The introduction of Table Format program groups enhances the clarity of program structure, it requires users to clearly identify and define the specific functional modules of the projects before start working with the table programming tool. Because of the merit of Table Format programming method, another interface specification procedure is required to document the interfacing relationship of Table Format programming groups or modules in order to allocate the programming job to a team of programmers. In the application of network communication or downloading, the pre-computer activities involves sending the Table Format program to a remote computer through a communication link or network.

The in process computer activity is performed by the Table Format compiler which translates the Table Format program into codes executable by a local computer, a target microcontroller, or a remote computer. The post-computer activity is usually a code executable by the target computer or microcontroller. This executable code can be further run by the computer or microcontroller to perform the function according to the original programming specification in order to carry out the programming objective. The compiled executable code is usually stored in memory means defined by RAM, ROM, any programmable no-volatile memory or any other storage devices commercially available. In the situation of microcontrollers to be used in consumer products, the memory means storing the compiled executable file is usually located in the article of sale rather than in the compiling computer. In this situation, the compiling computer simply acts as a development system, a compiler or a program supplier for a remote computing device.

Exhibit 1009, Page 1009

BRIEF DESCRIPTION OF DRAWINGS

Figure 1 illustrates the structure of an embodiment of a table format program.

Figure 2A is a table defining the table format expressions to be equated to user defined expressions.

Figure 2B demonstrated how a name is assigned to the table of Figure 2A.

Figure 3 is a table formed to define different printing styles of words to be identified.

Figure 4 is an embodiment showing different task states of a task table.

Figure 5A is an embodiment showing multiple task tables in a program.

Figure 5B is the numbered representation of Figure 5A.

Figure 6A is a program showing an embodiment of a table format program group.

Figure 6B is a further development of the embodiment of Figure 6A.

Figure 7 is a table showing a list of files to be included.

Figure 8 is a further development of the table of Figure 7.

Figure 9 demonstrates some proposed lengthy and descriptive instruction commands to be used in table format programming.

Figure 10 demonstrates a prior art fundamental table format program suitable for use in voice generating microcontrollers.

Figure 11 is a block diagram showing a local computer downloading a table format program to a remote computer through a communication link.

DESCRIPTION OF THE PREFERRED EMBODIMENTS

Throughout the following detailed description, similar reference characters refer to similar elements in all figures of the drawings.

The initial attention is directed to Figure 10 for a review of fundamental table format programming disclosed in issued US Patent 5,867,818. The table format program consists of two parts, a state table and a path table to program a sound generating programmable controller. In the second line of the state table, the order of the corresponding input trigger pins of the controllers is defined. Each of State0 to State 4 defines a possible trigger state of the controller. For example, the R:Path1 element corresponds to TG1 of State0 indicates that if a rising edge (represented by "R") is detected, the path named Path1 is executed. In Path1, the active state is changed to State1, then a sound named "Sound1" is generated. After the sound is completed, the control is loop back to Path1 and another round of sound generating sequence is started until a falling edge trigger (denoted by F:Path11) is received by TG1 during the State1 state. This example program demonstrates a "level hold" function of TG1 to TG4. That is, a sound is generated when one of TG1 to TG4 is compressed. The sound will be looped until the trigger button is released.

Attention is then directed to Figure 1 which depicts the structure of a table selected in an embodiment of an improved table format program. The keyword 101 indicates the starting of a program. The name of the program can be assigned by the programmer in a position next to the keyword. Keyword 106 indicates the start of a table listing the program modules to be included. Keyword 107 indicates the starting of a table defining constants to be used in the program. Keyword 102 declares the variables to be used in the program. Keyword 103 is a table enlisting the user-defined equivalents of commands and syntax expressions. In order for the program keywords to be distinguishable, the programmer may define the keyword printing style in a table starting with keyword 104. All the above features provide the preparation work of the table format program. The task table 105 provides one or more task states to define which task are to be activated, paused or terminated. Program group 111 comprises of an optional qualifier table 112, at least one state table 113 and one path table 116. The actual program

job involves interactively composing the content of the state table and the path table. Additional output state table 115 and output equation table 114 can be added to further define the output state(s) of the program. Finally, a library table 121 can be added to furnish commonly used command strings and routines. It should be noted that all the tables described above are not required to be listed in sequential order and many tables may be established to provide optional features. In addition, a program may contain multiple similar type of tables as in the situation of the task tables and the state tables. Names of the tables can be freely assigned following the colon sign of the keywords to better present the meaning of the program. Keywords used in table format programming may have more than one word and may refer to instructions, specific variables, constants and hardware elements of the system. It should be noted that the keywords provided are exemplary and alternative names of keywords can be used. Besides, some reasonable variation of the scope of the tables is always possible and are considered to be within the scope claimed in this application.

With reference now to Figure 2A, the table 200 represents a detailed example of the table 103 of Figure 1. The keyword 201 "Custom Expression" represents the start of this functional table which lists the user-defined equivalence of the official table format expressions. For example, assuming the official table format expression for the logical AND function is "AND" of 203; a programmer who prefers C language programming is free to substitute the table format command "AND" by "&&", 202 as taught by the C language instruction set. Custom expressions are applicable to any word or symbol set used by the language or system, such as instruction commands, symbols and system keywords. It is recommended that a comment 204 is provided every time an alternative expression is defined. The advantage of this function is to provide a personalized support to the programmer so that the familiar notation or expressions can always be used. However, when the composed program is printed, it is preferable for the compiler or editor to print out the program listing with the official expressions so as to facilitate the reading of program listing by other people. A similar application concept can be extended to a program editor. The official expression can be displayed as soon as the user-defined expression is keyed in. A switching function between the predetermined official or user-defined expression is also recommended, so the user can have a choice for the program

being displayed or printed in the official form or custom form. With this custom expression defining feature, meaningful lengthy expression names can be used to make the program listing more readable by other people while keeping the advantage for the programmer to used abbreviated expressions or short length symbols to represent instruction commands and syntax. Groups of official syntax and the corresponding group of user-defined equivalent syntax and labels can be built in the table format program with the commonly known table look up method. A typical application example is illustrated in Figure 9 where some lengthy and descriptive instruction set is shown. The shift operators "BIT SHIFT LEFT" and "BIT SHIFT RIGHT" clearly describe the operation to be performed. However, these instructions are too long and not welcome by skilled programmers. The programmer may then equate the "BIT SHIFT LEFT" instruction to the concise but less descriptive "<<" instruction used in the C language. The long instruction names make the program easily understood but too clumsy for skillful programmers. The "Custom Expression" table effectively resolve the concern while maintaining the advantages of the long instruction expressions.

Because the range of symbols available on the QWERTY keyboard is very limited, it is difficult to find sufficient meaningful symbols from the keyboard to compose a new programming method, like the table format programming method, without conflicting with traditional use of notations and symbols of some other popular languages. The custom expression table serves as a way to ease this problem by enabling users to reconfigure instructions and symbols according to their preference.

Element 210 illustrated how a name "MySign" is assigned to the "Customer Expression" table. A name is required particularly when more than one set of "Customer Expression" tables are provided to enable more than one users to manipulate or read the program. For example, in addition to the table "MySign" to be included in the program, another custom table named as "JohnsSign" can be added in the same program. If John wants to read the program, simply set the "JohnsSign" expression table as the default displaying table, and John will see the program displayed in his favorable format. The novel feature of this table is to enable every user to include their own set of custom defined expressions so as to facilitate converting or editing the program in their preferred format.

Although the table illustrated in Figure 2A provides a method to reconfigure keywords and expressions, it is desirable to have a simple method to provide alternate expression or commands of another language for a small job. This can be achieved by specifying a symbol representing a specific language in front of expressions specified by that language. Figure 2B illustrates how the content of Register A is to be masked with the binary number 00001111 so as to obtain the last four bits of the Register A content, and then proceed to display this number. The expression 216 provides the element "(C:&&)" to indicate that the "&&" instruction is an instruction of the "C" language. The masked value of Register A is then displayed using a predefined "Display" command. Alternatively, an expression performing the same function may be obtained by using the "&" instruction of the assembly language of a microprocessor. The sign "A: ()" is an expression to indicate the operation included in the brackets is written in assembly language. These two methods although convenient, is not as powerful as the table of the custom expression table of Figure 2A which personalized the expressions of a whole program is to be converted or to be personalized.

Since table format programming involves a large amount of discrete user-assigned labels, and these labels are scattered around the program and mixed with keywords and instruction commands, it is difficult for a user to read the program, since the user is unable to distinguish a user-assigned label from other keywords and instruction commands. It is therefore preferable to provide means to identify these labels to make the program more user friendly. Figure 3 demonstrates a table of the program which controls how the user assigned labels are to be presented or highlighted. The table element 234 provides the selection of case. Typical cases available for selection are title case, all upper case and all lower case. The table element 239 indicates the choice of the letter style. Typical choices available are bold, italic and underline. It should be noted that both elements 234 and 239 offers excellent identification for all kinds of displaying device including black and white printers. Keyword 231 indicates the starting of the identification style definition section of the program. It is recommended that a user assigned name is placed after the keyword 231 to indicate that the following setting is the preferred setting of a particular person. Multiple identification style tables assigned according to the preference of different people can be included in the program and each

table assigned a name as represented by element 232. A selection of one of the identification style tables as active, sets the printing style to suit the preference of the particular user reading the program.

In order to present well structured program flow in a multitasking environment, a task control table (hereafter referred as task table) is introduced in the embodiment demonstrated in Figure 4. Element 261 is a keyword to identify a task table. Element 262 is a label assigned to name the task table. This name is provided in order to differentiate from other task table if two or more task tables are required. Elements 263, 264 represent different tasks or programs which may be run under the control of the task table. Each row of the table represents a task state. In each task state a task condition is assigned to represent the condition of each task. Listed below are some examples of expressions to describe a task condition:

Start: indicates that irrespective of whether the task or program is running, paused or had been terminated, the program is restarted from the beginning;
Continue: indicates that if a task had already been started, then it continues to run;
Pause: indicates the task or program to be paused;
Run: indicates if a task has not yet been started, it is then started; if a task or program is running, it continues to run; if a task is paused, the running of the task is resumed;
X: indicates the running of the task is terminated.

In the row 265, the task "Task Status 1" instructs the "Main" program to start running while programs 2 to n are in terminated condition. In the task state 268, all the programs are instructed to run. It should be noted that for each task table, only one task state is assigned to be active at a time. In systems with limited resources, it is important to assign priority to the active task running. The task states 273 and 274 assign priority to the tasks. It should be noted that a separated table can be established just to describe priority assigned to the tasks. Because the title elements of each element of the activity task table and priority task table are identical, it is possible to combine the two types of task tables into one as shown in Figure 4. In this situation, two active task states are required, one for the task activity status and one for the task priority assignment.

Figure 5A illustrates a real world application example to demonstrate the concept of the task table. Figure 5B is the numbered illustration of Figure 5A. This example comprises three task tables. The first task table is named as "Input" as shown in element 301. It comprises three programs named "Keyboard" 302, "Mouse" 303 and "Gameport" 304. "Keyboard" is a program scanning the keys of a keyboard. "Mouse" is a program decoding the motion of the mouse while "GamePort" inputs the triggers signals obtained from a game port. In the task state 305 named "All", all the three programs are running to enable the computing device to be responsive to all three input devices. In the task state 306 named "Normal" only the keyboard and mouse are enabled. The game port is not used so as to improve the servicing efficiency of the computing device. When in the game playing mode, the task state 309 named "Game" becomes the only active program or task. The keyboard and mouse are not used so as to let the computing device focus all its resources on servicing the game play. The second task table is a table named "Ports" 321 which controls the serial and parallel ports of the computing device. The third task table is named "Device" as shown in element 341. It controls the driver programs to operate the "CDRom" 342, the "HardDriveC" 343, and the floppy disk drive. When the task state "ReadCD" 345 is activated, the CD Rom and the Hard Drive C driver programs are activated but the floppy disc driver program is terminated. In the mode demanding full running speed of the hard drive, the task state "HDFullSpeed" 346 becomes the active task state where the hard drive becomes the only running device. According to this application example, it is recommended to group only similar nature or interrelated tasks to form a common task table. It should be noted that only one task state from each task table is to be assigned to be active at any time.

With reference to Figure 6A, a main group of table format program is demonstrated. The program is named as "WebSale" which provides the skeleton structure of a sales program to be provided through the internet. This example illustrates the concept of multiple languages programming under the table format programming environment. The line numbers of the program are only inserted to facilitate the description of the embodiment. It should be noted that the states and path equations are not required to be in sequence. Attention is now drawn to line 1. A keyword "Group" indicates the starting of a table format group or program module. The name of the group

is "Main". Main is assigned as a key word to indicate that this group is the first group of programs to be executed when the program starts to run. Line 2 starts with a keyword "qualifier" which defines the qualified conditions of the qualifiers enlisted in a configuration state. In lines 3 to 6, the term "Icon" applies to a functional command which constructs an icon and provides a trigger to the configuration state when this icon is clicked. In a typical arrangement of table format programming icons are numbered serially. Each icon is defined and named in the qualifier table. As an example, when Icon(1) is equated to the name "Catalog", the word "Catalog" is assigned and displayed to the first icon. In fact, once a name is assigned to an icon, the number it carries is immaterial unless it the term "icon(n)" is mentioned in a program and "n" is a result of a computation. Line 8 defines an input states configuration table named "FirstPage". There are five qualifiers assigned in this table namely "Catalog", "Purchase", "Service", "Home" and "Quit". Each qualifier refers to the trigger of an icon as defined in the qualifier table of line 2. The first input qualifier state is named "Ready" as in line 9. In this state, when a qualified trigger representing the icon "Catalog" is received, the path named "P_catalog" is executed and similarly for the other qualifiers. The other input configuration state is named "Hold1" as indicated in line 10. An "x" in the state equation indicates that the corresponding qualifying condition is in a don't care condition, for which a trigger is blocked or no response is required when a qualified trigger comes in.

Attention is now drawn to line 11 of Figure 6A which provide another configuration table named "Response". It is an output states configuration table with five elements. The first four elements come with a keyword "Group:" indicate a program composed as a group. The name of the group assigned following the colon sign. The first group is named "Info" which provides product information. The second group "Order" is a program which guides the user through a purchasing process such as registering a credit card number, product number, order quantity, total amount, options, then encrypts the data and send the order to the supplier for decoding. The third group "Service" provides general interactive customer service facilities. The forth group "Register" registers the customer information. The last element of the output configuration is a hardware port P3.1 which connects to a speaker of the computing device. When a code P+ is assigned to this port, a burst of positive going pulses is sent to the speaker and a beep tone is heard.

Port P3.1 is a hardware terminal and is therefore classified as an hardware oriented output. All the first four groups are software oriented output conditions and are classified as virtual computing outputs. Any output condition not related to a hardware output terminal is defined as a virtual computing output. The definition of virtual computing output includes any non-terminal related activity which generates data, displays or produces signals or information, initiates a program, resets a program, starts a software timer or counter, or manipulates an internal circuitry such as a register etc. Lines 12 to 17 are the output states configured under the state table "Response". When a "Run " command appears in an output configuration state, the corresponding group program is run. When a "Continue" instruction is received, a running program continues to run or the program remains idle if it had not yet been started or in a pause condition. The "x" sign indicates no output action is required. With all these description, the action of the output states in lines 12 to 17 are self explanatory. It should be noted that more than one input or output state table may be present in a program. Multiple state tables simplify the structure of the tables and make the programming job easier. However, it should be noted that only one of the configuration states of every input state table should be specified to be active at a time. As a programming trick, the interrelated inputs qualifiers and output conditions can be combined to form a state table. It should also be noted that input states and output states can also be combined to form a mixed state if desirable.

Line 18 starts the action "Path(s)". Each path defines one or more actions to be executed when the path name is recited in any qualifier element of a configuration state. Line 19 is a path named "Start" which is the default starting path when the Group is executed. The programming procedure starts by reciting the desirable action when the program is first started. The starting action "CheckSystem" checks the configuration of the local computing system such as the display drivers, physical port to drive the speaker and the system resources available to run the program. The resources available in the local computing devices to be evaluated includes the computer time, number of registers, amount of memory available, memory configuration, timer and counters occupied, interrupt channels available and any specific hardware circuit configuration. Included in the "CheckSystem" action should be a procedure to reconfigure the down loaded program according to the parameters of the system. The next step is to display the first page. This

action is simply defined as "DisplayFirstPage" in the program. A "Beep" sound is then generated. "Hold2" indicates all output configurations are put on hold as directed by the "Response" state table. The "Ready" instruction initiates the "Ready" state of the input state table "FirstPage". During the "Ready" state, whenever a qualified trigger of the icons "Catalog", "Purchase" or "Service" is received, one of the corresponding paths 20 to 22 is executed. In each of these paths, a window directing the action is displayed and a further sales promotion program can be started. "BuySolicit" is an interactive program to solicit sales of the company products. During the path "P_purchase", the actions "Hold1" and "Hold3" limits the response allowable from the local user terminal to the icons "Home" and "Quit". The "GreyButton" is an action to change the color of the icons which are disabled such as the icons "Catalog", "Purchase" and "Service" as directed by the state command "Hold1". When the path "Bye", line 23 is executed, the program "Terminate" run and the program is ended. A keyword "EOG" which means "End Of Group" is placed at the end of the group to inform the compiler that the program group ends here.

It can be observed that the programming method discussed interactively describes the activities of the program according to the composed states and paths. Meaningful programmer-assigned terms such as "Beep", "CheckSystem" and "Terminate" are used. This procedure is as natural as writing an essay to precisely describe the actions required by the program.

In compiling this program, many programmer-assigned terms remain unidentified such as "CheckSystem", "DisplayFirstPage" and "BuySolicit". All these programmer-assigned labels are not executable by the computing device unless they are further linked to an executable program. A next step demonstrated in Figure 6B is then required to further define the description of the unidentified labels. Typical methods to make these labels executable is to link them to an external executable program or further define the label with a program derived from a library. It is very desirable that the compiler provides a function to identify all programmer-assigned labels. The identification is preferably distinguishable when the program is printed by a black and white printer. Typical preferable identification methods include a change of case styles, and font style such as bold, italic, or underline. The requirement of each of the unidentified label is then

analyzed and a most appropriate programming language is selected to compose a program to provide the desirable action.

The supporting programs can be written in any languages or even by another table format program. These supporting programs are then "Included" in the program for the compiler to put the programs together. In line 19 of Figure 6B, it indicates that the "CheckSystem" is preferably a program written in Java as denoted by the prefix "EJ" where "E" means it is an external program to be included. The action "DisplayFirstPage" is preferably written in Visual Basic. In line 19, the action "BuySolicit", a window to solicit purchasing is preferably derived from a local or global library. Line 24 indicates the starting of the local library. Line 24 is a further elaborated path equation which describes the action to solicit the customers. This action includes executing an external program "CheckRecord" written in "C" and a program named "SolicitWindow" written in Visual C++ to interactively solicit the customer to buy products. The element "GreyButton" in line 21 refers to a path positioned in the local library which comprise a "C" program to identify which icon is assigned an "x" and then another program written in Visual Basic to turn these icon into a gray color to indicate that these icons cannot be triggered.

When an abundant library of supporting programs is built up, a programmer familiar with table format programming may start the programming job by selecting and including various common supporting programs. Figure 7 indicates an exemplary include table when a program involving windows and transceiver is to be composed. The supporting programs in this include table are mandatory and therefore it is desirable for the compiler to outline any included program not being used in the composed program.

There are more technical requirements to make use of the table format programming method to administrate the supporting programs written in other languages, such as the method to pass parameters and equate variables between the programs. Proper management of the different types of program is also to be considered especially if they are to be translated by different compilers. Those skill in the art will appreciate the advantages provided by the table format programming method disclosed and be able to set up a compiling system to complete the actions required. For example, predefined registers or memory blocks may be assigned to handle passing of a parameter when a

particular external program is included. In addition, a management program is required to handle the assignment and release of computing resources when different functional modules or different languages are included in the program.

Attention is now drawn to the following major advantages of table format programming summarized from the teaching of the above demonstrated examples:

1. The program can be composed easily by writing descriptive labels.
2. The program is well-structured so that the chance of incorporating program bugs is small.
3. It is simple for hardware terminals to be combined with virtual software outputs and interact with sophisticated program flow.
4. The table format program provides a clean-cut presentation and can easily be read by any third party. The user-friendly presentation and the clarity provided are important to further reduce program debug time and also minimize future maintenance cost.
5. Table format programming method offers great simplicity to structure a program by making use of multiple languages. These languages are selected according to the application environment and features offered by each language.
6. The concise program written in table format provides a high data compression ratio and makes the program ideal for transmission from a remote host terminal to a local computing device through a communication channel of limited bandwidth and data handling rate.

The preferred embodiments of the invention described herein are exemplary and numerous modifications, specification variations, table rearrangements, instruction and keyword assignments can be readily envisioned to achieve an equivalent result, all of which are intended to be embraced within the scope of the appended claims.

CLAIMS

What is claimed is:

1. A programming tool for programming a first computing device comprising a first table having a first plurality of configuration states, at least one of said configuration states defining one or more qualifying conditions;

a second table specifying a second plurality of paths, at least one of said paths being executed when a qualifying condition listed in said first table is satisfied; and one of said configuration states being specified as an active state; characterized in that said tool further comprises one or more of the following elements or characteristics:

(1) at least one of said configuration state comprising a virtual qualifier;

(2) at least one of said second plurality of paths having one or more labels, wherein each label represents an executable program;

(3) one or more further tables specifying user-defined custom expressions to represent corresponding predefined instructions of a programming language;

(4) said first computing device being a remote computer connecting to a second local computing device through a communication link; wherein the digital data representing said first and second tables is stored in said remote computing device for downloading to said local computing device through said communication link;

(5) said first computing device comprising at least a first processor and a second processor; wherein said first processor is configured to translate the relationship between said first and second tables and to give direction to the second processor to execute other programs;

(6) at least one of said configuration states comprising a label and said label being equated with a separate expression defining the qualifying condition represented by said label;

(7) a table defining a preferable style of keywords;

(8) a table having a third plurality of task states to define the activity of a fourth plurality of tasks;

(9) a table defining a fifth plurality of output expressions, each output expression representing an output condition of a configuration state or a path element listed in said second table.

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(10) a further state table defining a sixth plurality of configuration states and a further path table defining a seventh plurality of paths; wherein said first and second tables are grouped as a first table group to perform a first function, said further state and path tables are grouped as a second table group to perform a different second function.

2. The programming tool of claim 1 wherein said tables and/or elements are configured by a programmer in accordance to a preprogram objective, and to be translated into a program executable by said computing device to deliver said preprogram objective.

3. The programming tool of claim 1 wherein said programming tool comprises a third computing device having a table format compiler configured to compile a table format program input into said third computing device to generate codes executable by said first computing device.

4. A computing program configured by a programmer in accordance to a predefined programming objective for executing by a computing device to deliver said programming objective comprising a first table having x configuration states, at least one of said configuration states is configured to define one or more qualifying conditions;

a second table specifying y paths, at least one of said paths is executed by said computing device when a qualifying condition enlisted in said first table is satisfied; said program further comprising at least one of the following tables:

(1) a table specifying user defined custom expressions to equate the corresponding predefined instructions of a programming language;

(2) a table defining preferable style of keywords;

(3) a table having m task states to define the activity of n tasks, at least one of said task states specifies k selected tasks to be active;

(4) a table having p task states to define q tasks, at least one of said task states specifies the priority of the active tasks to be serviced;

(5) a table defining x qualifying expressions, each qualifying expression represents a qualifying condition of a configuration state; and

(6) a table defining y output expressions, each output expression represent an output condition of a configuration state or a path element enlisted in said second table.

5. The computer program of claim 4 further be compiled into data executable by said computing device, and to be stored in memory means configured to store digital data.

6. The computer program of claim 5 further be embedded in said computing device wherein said computing device is installed in an article of sale.

7. A programming method to program a computing device responsive to one or more predefined qualifying conditions, to execute one or more paths and to direct said computing device to perform in accordance to the objective of said paths; said programming method comprises the steps of:

(1) specify x configuration states;

(2) specify one or more qualifying conditions to at least one of said x configuration states,

(3) specify y paths to be executed by said computing device;

(4) assign z labels to represent one or more of the path elements of step (3) wherein z is an integer equal or greater than one;

(5) for each qualifying condition of step (2), specify a path of step (3) to be executed by said computing device when a specific qualifying condition is satisfied;

(6) specify at least one of the configuration states to become the active configuration state;

(7) for each assigned label of step (4), define an executable program to be represented by said label.

8. The programming method of claim 7 wherein the label of step (4) is not initially executable by said computing device and the additional step (7) is configured to enable the execution of said label by said computing device.

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9. The programming method of claim 8 further comprising a step to identify a label not executable by said computing device for the composing of the program of step (7) thereof.

10. The programming method of claim 7 wherein the program of step (7) is composed by any available programming languages including the table format programming language.

11. The programming method of claim 10 further comprising a step to define means to pass parameters between the program represented by the steps (1) to (6) and the executable program represented by the label.

12. The programming method of claim 7 further comprising a step to specify one or more output conditions to at least one of said x configuration states.

13. The programming method of claim 7 wherein at least an output condition is specified in a path or in a configuration state.

14. The programming method of claim 7 further comprising a step to specify resources of said computing device required to service the states and paths thereof.

15. The programming method of claim 7 further comprising a step to indicate the resources of said computing device available for composing the program of step (7).

16. The programming method of claim 7 wherein said computing device comprises of two or more processors; the resources to service the states and paths specified are provided by a first processor and at least part of the program of step (7) is serviced by the resources of a second processor.

17. The programming method of claim 7 wherein said computing device is defined as a first computing device; said programming method further comprising a step to receive

digital data representing the aforementioned steps through a communication link from a second computing device locating remote from said first computing device.

18. The programming method of claim 7 wherein at least part of said steps are organized into a table format.

19. The programming method of claim 18 further comprising a step to group the configurations states of step (1) and (2) into one or more tables.

20. The programming method of claim 18 wherein the configuration states or paths are not necessary to be enlisted in sequential relationship to each other.

21. The programming method of claim 7 further comprising a step to transform the specifications of the configuration states and the paths into digital data to be stored into said computing device for the execution thereof.

22. The programming method of claim 7 further comprising a step to transform at least part of the specification of said steps with a secondary programming language of different format.

23. The programming method of claim 7 further comprising a step to identifying the location of the program composed by said steps.

24. The programming method of claim 10 wherein the program of step (7) is a program locates external to the program composed by the steps of claim 1.

25. The programming method of claim 24 further comprising a step to identify the location of said external program.

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32. The programming method of claim 29 further comprising a step to organize a keyword to represent the steps (1) to (3).

33. The programming method of claim 29 wherein said task states are not necessary to be listed in sequential relationship to each other.

34. The programming method of claim 29 wherein the steps (1) to (3) define a first task table, said programming method further comprising steps to define a second different task table.

35. A programming method to program a remote computing device in accordance to a first predefined pre-computer objective, for said remote computing device to perform said predefined objective, said programming method comprises the steps of:

(1) specify x configuration states, at least one of said configuration states defines one or more qualifying conditions;

(2) specify y paths to be executed by said computing device;

(3) for each qualified condition of step (1), further specify a path to be executed by said computing device when a specific qualifying condition is satisfied;

(4) specify one of the qualifier configuration states to become the active configuration state;

(5) store the digital data representing the aforementioned steps in a local computing device; and

(6) download the digital data of step (5) to said remote computing device though a communication link.

36. The method of claim 35 wherein said communication link is a network.

37. The method of claim 36 wherein said network comprises of the internet; intranet extranet or LAN.

38. The method of claim 35 further comprising a step to direct an element of a path to a program written in a second programming language of different format.

39. The method of claim 35 further comprising a step to evaluate the architecture of said remote computing device and a further step to configure the aforementioned steps to work with the architecture of said remote computing device.

40. The method of claim 35 further comprising a step to organize at least part of the data specified by said steps into a table format.

41. The method of claim 35 further comprising a step to store digital data representing said configuration states and paths into said remote computing device for the execution thereof.

42. The method of claim 35 further comprising a step to transform at least part of the specifications of the configuration states and the paths into a secondary programming language of different format.

43. The method of claim 35 wherein the configuration states or paths are not necessary to be listed in sequential relationship to each other.

44. A multiple processors computing device comprising a first processor and a second processor wherein said first processor is configured to execute at least part of a table format program defined by m configuration states interact with n paths.

45. The multiple processors computing device of claim 44 wherein the second processor executes programs written with a second language not in table format.

46. The multiple processors computing device of claim 45 wherein the second processor is configured to execute a program as directed by a table format program executed by said first processor.

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47. The multiple processors computing device of claim 44 wherein said first and second processors are included in a single integrated circuit.

48. The multiple processors computing device of claim 44 wherein at least one instruction executable by one of the processors is different from the instruction set executable by the other processor.

49. The multiple processors computing device of claim 44 further comprising means to pass parameter between said first and second processors.

50. A method to compose a compiler suitable for a first computing device to compile a table format program having m configuration states and n paths, and to generate a program suitable to be executed by a second computing device, comprising the steps of:

(1) define a table format programming specification as a pre-computer activity;

(2) identify the region representing the configuration states;

(3) identify the region representing the paths;

(4) identify at least one qualifying condition of a configuration state and link it to the path specified; and

(5) link a configuration state A to a path quoting said configuration state A as its element.

(6) generate codes suitable to be executed by said second computing device.

51. The method of claim 50 further comprising a procedure to integrate the function of steps (1) to (4) into a compiler of an existing language.

52. The method of claim 50 further comprising a procedure in step (1) to identify a keyword representing the starting of the configuration states.

53. The method of claim 50 further comprising a procedure in step (2) to identify a keyword representing the starting of the paths.

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54. The method of claim 50 further comprising a step to equate an user defined expression with a specific instruction of the table format programming language.

55. The method of claim 54 further comprising a step to identify a table equating user define expressions with predefined instruction set of table format programming language.

56. The method of claim 50 further comprising a step to identify label unexecutable according to the instruction set of said table format program.

57. The method of claim 56 further comprising a step to link said unexecutable label to an external program.

58. The method of claim 57 wherein said external program comprises of a program composed by any available programming language including the table format programming language.

59. The method of claim 50 further comprising a step to distinguish two or more program groups, wherein each program group comprises of at least one configuration state table and one path table.

60. The method of claim 59 further comprising a step to compile the interaction in between said multiple program groups.

61. The method of claim 57 further comprising a step to define an instruction to activate a program group.

62. The method of claim 61 wherein said instruction is represented by one or more task tables.

63. A method to program a computing apparatus to perform in accordance to a pre-computer programming objective, comprising the steps of:

(1) select a programming language having a predefined instruction set;

(2) define alternate expression to represent a specific instruction of the selected language;

(3) compose program with said alternate expression and

(4) configure said program to be executable by said computing apparatus and directing said computing apparatus to perform in accordance to said pre-computer programming objective.

64. The method of claim 63 wherein the selected programming language is a table format programming language comprising:

x configuration states, at least one of said configuration states defines one or more qualifying conditions;

y paths to be executed by said computing device; and

one of the paths is executed by said computing device when a specific qualifying condition is satisfied.

65. The method of claim 63 wherein the process of step (4) is a translation process carried out by an editor, a compiler, an interpreter or a translation program.

66. The method of claim 65 further comprising the step of displaying the composed program with the translated predefined instruction set of the selected language.

67. The method of claim 65 further comprising the step of displaying the composed program with the user defined expressions.

68. The method of claim 63 further comprising a step to provide a table located inside the program for linking said alternate expression with the corresponding specific instruction.

69. A programming method to program a computing device responsive to one or more virtual qualifiers, to execute one or more paths; said programming method comprises the steps of:

(1) define one or more virtual qualifiers;

(2) specify x configuration states, at least one of said configuration states defines one or more qualifying conditions;

(3) define at least one of the qualifying conditions of step (2) to represent a virtual qualifiers;

(4) specify y paths to be executed by said computing device;

(5) for each qualifying condition of step (2), further specify a path to be executed by said computing device when a specific qualifying condition of said qualifier is satisfied;

(6) specify one of the qualifier configuration states to become the active configuration state.

(7) configure said aforementioned steps into a program executable by said computing apparatus.

70. The programming method of claim 69 wherein said computing device is defined as a first computing device; said programming method further comprising a step to receive digital data representing the aforementioned steps through a communication link from a second computing device locating remote from said first computing device.

71. The programming method of claim 69 further comprising a step to specify one or more configuration states to comprise an output configuration.

72. The programming method of claim 71 wherein said output configuration defines the output conditions of one or more output terminals of said computing device.

73. The programming method of claim 71 wherein said output configuration defines a virtual computing output generated by said computing device.

74. The programming method of claim 69 further comprising a step to transform the specifications of the configuration states and the paths into digital data executable by said computing device.

75. The programming method of claim 69 further comprising a step to transform at least part of the specifications of the configuration states and paths with a secondary programming language of different format.

76. The programming method of claim 69 wherein the x configuration states and y paths are not necessary to be listed in sequential relationship to each other.

77. The programming method of claim 69 further comprising a step to provide a keyword for identifying the location of the program composed by said steps.

78. The programming method of claim 69 further comprising a step to group the configuration states of step (2) into one or more tables, and one of the configuration states in each table is specified to be active.

79. The programming method of claim 69 further comprising a step to organize a separated table to determine if the program composed by steps (2) to (6) is active or not active.

80. The programming method of claim 69 further comprising the following steps:

(8) provide a predefined instruction set to program the paths and configuration states;

(9) define alternate expression to represent a specific instruction of the instruction set of step (8);

(10) compose program with the alternate expression and

(11) configure said program to be executable by said computing apparatus.

81. The method of claim 80 wherein the process of step (11) is a translation process carried out by an editor, a compiler, an interpreter or a translation program.

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82. The method of claim 80 further comprising a step to provide a table for cross reference between said alternate expression and the corresponding specific instruction.

83. The method of claim 69 further comprising a step to specify a configuration state to be active.

84. The method of claim 69 further wherein a path is defined as a default path to be executed at initialization.

85. A programming method to facilitate the identification of a predetermined type of custom expressions in the program listing of a computing device comprising the steps of:

(1) define one or more sets of custom expressions;

(2) equate the custom expressions of step 1 with a reference set of expressions;

(3) select a desirable set of expressions from a predetermined number of different expression sets derived from the steps (1) and (2); and

(4) list said program listing with the selected expression set.

86. The programming method of claim 85 further comprising the steps of

(1) defining x configuration states, at least one of said configuration states defines one or more qualifying conditions;

(2) define y paths to be executed by said computing device; and

(3) assign one of the paths to be executed by said computing device when a specific qualifying condition is satisfied.

87. The programming method of claim 85 wherein said custom expression set is defined by a table having a keyword.

88. The programming method of claim 85 wherein said predetermined expression set is configured to highlight user defined labels.

89. The programming method of claim 88 further comprising a step to select one of the different ways to highlight said selected custom expression set.

90. A programming method to program a computing device responsive to one or more qualifying conditions, to execute one or more paths; said programming method minimally comprises the steps of:

(1) define a programming objective;

(2) specify x configuration states in accordance to said programming objective wherein x is an integers equal or greater than one;

(3) specify one or more qualifying label to at least one of said x configuration states,

(4) directs the qualifying label of step 2 to a separated expression describing the qualifying condition or conditions represented by said label;

(5) specify y executable paths in which at least one path is executed in response to a qualifying condition specified by step (4), and

(6) configure said aforementioned steps into a program executable by said computing apparatus to perform said programming objective.

91. A first computing apparatus connected with a second remote computing device comprising:

computing means to execute a program; and

memory means storing digital data executable by said first computing apparatus or said second remote computing device; wherein said digital data comprising representation of a table format program having x configuration states and y paths; at least one of said configuration states defines one or more qualifying conditions; and one of said path is executed by said computing means when a specific qualifying conditions is satisfied.

ABSTRACT

Table format programming method suitable for use with multiple languages and download through internet and network environments provides an optional table (103, 200) to define custom expressions, an optional table (105, 260) to specify multitask applications, an optional table (104, 230) to specify printing style of labels and a table of configuration states (113) which interact with a table of paths (116). An optional qualifier table (112) defines qualifying conditions of an input configuration state. Hardware embodiments includes a coprocessor to handle the table format programming process and relieve the main processor for other applications. During programming, programmers provide user defined labels to describe the function and action desired. The labels are then further developed in additional groups of program.

Serial No. 09/475,076

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PATENT & TRADEMARK OFFICE

DECLARATION FOR PATENT APPLICATION

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are stated below.
I believe I am the original, first and sole inventor of the subject matter which is claimed and for which a patent is sough on the invention entitled Table Format Programming the specification of which

(X) is attached hereto.
() was filed on _____ as Application Serial No. _____
() was amended on _____ (if applicable).

I hereby state that I have reviewed and understand the contents of the above - identified specification, including the claims, as amended by any amendment referred to above. I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, Section 1.56 (a),

I hereby claim the benefit under Title 35, United State Code, Section 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, 112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, section1.56(a) which occurred between the filing date of the prior application and the national or PCT international filing date of this application:
Application Number: 08/538,426 Date Filed: Oct 02, 95 Status: Issued as 5,867,818 and
Application Number: 09/169,462 Date Filed: Oct 09, 98 Status: Pending Abandon

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

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